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"5706224".PN..PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	2
(5706224.PN.).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	2

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L9	5706224.pn.		2	L9
L8	(effective) near5 address\$3 near12 load near12 store near35 (base or offset\$1)		7	L8
L7	(effective) near5 address\$3 near12 load near12 store near35 offset\$1		3	L7
L6	(effective) near5 address\$3 near12 load near12 store near35 (lookup or look\$4 near up)		0	L6
L5	(effective) near5 address\$3 near12 load near12 store near15 (lookup or look\$4 near up)		0	L5
L4	(effective) near5 address\$3 near12 load near12 store near25 (strid\$3 or differenc\$3 or relation\$6 or offset\$1)		4	L4
L3	L2 and pipelin\$8		149	L3

<u>L2</u>	(effective) near5 address\$3 near12 load near12 store and (strid\$3 or differenc\$3 or relation\$6 or offset\$1)	192	<u>L2</u>
<u>L1</u>	(effective) near5 address\$3 near12 (eliminat\$5 or avoid\$5 or "no" or without or "not" or bypass\$5 or skip\$4 or prevent\$5) near8 (calculat\$3 or computing or computed or compute or computes or computation\$1) near6 address\$3	73	<u>L1</u>

END OF SEARCH HISTORY

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Term	Documents
(20 AND 16).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	20
(L20 AND L16).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	20

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<u>L22</u> L20 and 116	20	<u>L22</u>
<u>L21</u> L20 and 115	102	<u>L21</u>
<u>L20</u> (match\$3 or compar\$8) near6 (referenc\$3 or specif\$6 or operand\$1) near6 address\$3 and 15	169	<u>L20</u>
DB=PGPB,USPT; PLUR=YES; OP=OR		
<u>L19</u> 15 and 115	325	<u>L19</u>
<u>L18</u> 15 and 116	147	<u>L18</u>
<u>L17</u> L16 and 11	119	<u>L17</u>
<u>L16</u> (711/200-221)[CCLS]	5773	<u>L16</u>
<u>L15</u> (712/2-300)[CCLS]	11224	<u>L15</u>
<u>L14</u> 11 and 113	133	<u>L14</u>
<u>L13</u> (712/200-221)[CCLS]	4555	<u>L13</u>

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<u>L12</u>	L11 not l2	46	<u>L12</u>
<u>L11</u>	l5 near18 (compar\$8 or match\$5)	48	<u>L11</u>
<u>L10</u>	L5 near75 (match\$3 or compar\$8) near6 (referenc\$3 or specif\$6 or operand\$1) near6 address\$3	4	<u>L10</u>
<u>L9</u>	L5 near75 (match\$3 or compar\$8) near6 operand\$1 near6 address\$3	1	<u>L9</u>
<u>L8</u>	L5 near55 (match\$3 or compar\$8) near6 operand\$1 near6 address\$3	1	<u>L8</u>
<u>L7</u>	L5 near55 compar\$8 near6 operand\$1 near6 address\$3	1	<u>L7</u>
<u>L6</u>	L5 and compar\$8 near6 operand\$1 near6 address\$3 (without or bypass\$3 or skip\$4 or avoid\$3 or eliminat\$5 or no or "not") near6	108	<u>L6</u>
<u>L5</u>	(computed or computing or computes or computation\$1 or calculat\$8) near7 address\$3	1462	<u>L5</u>
<u>L4</u>	L1 near50 compar\$8 near6 operand\$1 near6 address\$3	0	<u>L4</u>
<u>L3</u>	L1 near35 compar\$8 near6 operand\$1 near6 address\$3	0	<u>L3</u>
<u>L2</u>	L1 and compar\$8 near6 operand\$1 near6 address\$3	98	<u>L2</u>
<u>L1</u>	(without or no or "not") near6 (computed or computing or computes or computation\$1 or calculat\$8) near7 address\$3	1153	<u>L1</u>

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IEEE JNL IEEE Journal or Magazine

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1. DSP data memory layouts optimized for intermediate address pointer updates

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